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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/527,380

03/11/2005

Johannes Lambertus Hendrikus Van Hees

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04/04/2006

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EXAMINER

VAZQUEZ, ARLEEN M

ART UNIT

PAPER NUMBER

2829

DATE MAILED: 04/04/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/527,380

Applicant(s)

VAN HEES, JOHANNES
LAMBERTUS HENDRIKUS

Examiner

Arleen M. Vazquez

Art Unit

2829

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 2/09/2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-9 is/are pending in the application.
- 4a) Of the above claim(s) 4-9 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1 and 2 is/are rejected.
- 7) ☒ Claim(s) 3 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 11 March 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. Applicant's election of Group *I.* claims 1-3 in the reply filed on February 9, 2006 is acknowledged. Because applicant did not distinctly and specifically point out the supposed errors in the restriction requirement, the election has been treated as an election without traverse (MPEP § 818.03(a)).

Claims 4-9 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected invention of Group *II.*, there being no allowable generic or linking claim. Election was made **without** traverse in the reply filed on February 9, 2006.

Claim Objections

2. Claims 1 and 3 are objected to because of the following informalities:

In claim 1, it is not clear what "an external power supply source" comprises of. The term "an external power supply source" has no antecedent basis.

In claim 3, it is not clear what is meant by the limitation of "a further incoming supply current is supplied to the power supply unit in parallel with said incoming supply current", therefore it is considered as indefinite. It is not clear what is meant by the limitation of "outside a part of the power supply unit that affects the output impedance", therefore it is considered as indefinite.

Appropriate correction is required.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1 and 2 are rejected under 35 U.S.C. 102(b) as being anticipated by ***Wilstrup et al. (US 5,773,990)***.

As to claim 1, ***Wilstrup et al.*** discloses in Figure 5 a method of performing IDDQ test using a power supply unit (205) to supply the power supply current to the electronic circuit (DUT), adjusting (using elements 402,404,406,410,412 and 418) an output impedance of the power supply unit (205) to a value selected for the electronic circuit (DUT), the value having been selected so that a resonance circuit (422) is substantially critically dampened, measuring IDDQ current with a current sensing element (416) that senses a value of the current supplied to the electronic circuit (DUT) between the power supply (205) and the power supply regulating circuit (402,404,406,410,412 and 418) that supplies power to the electronic circuit under test (DUT).

As to claim 2, ***Wilstrup et al.*** discloses in Figure 5 a power supply unit (205) contains regulating loop (402,404,406,410,412 and 418), for regulating a supply voltage applied to the electronic circuit (DUT) at least during measurement of the IDDQ current, said measuring being performed on an incoming supply current that the power supply unit (205) draws to provide the regulated voltage to the electronic circuit (DUT).


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5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Arleen M. Vazquez whose telephone number is 571-272-2619. The examiner can normally be reached on Monday to Friday, 8am to 5pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on 571-272-1705. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

AMV


VINH NGUYEN
PRIMARY EXAMINER
A.U. 2829
04/03/06